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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Mes** | Nov 2016 | Dic 2016 | Enero | Febrero | Marzo | Abril | Mayo | Junio | Julio | Agosto | Septiem | Octubr. | Noviem. |
| **Actividad** |
| Investigación | **X** | **X** | **X** | **X** | **X** |  |  |  |  |  |  |  |  |
| Diseño de aplicación |  | **X** | **X** |  |  |  |  |  |  |  |  |  |  |
| Diseño de los circuitos en ISIS |  |  | **X** | **X** | **X** |  |  |  |  |  |  |  |  |
| Diseño de los circuitos en ARES |  |  | **X** | **X** | **X** |  |  |  |  |  |  |  |  |
| Preparación de baquelitas |  |  |  |  | **X** | **X** | **X** | **X** |  |  |  |  |  |
| Montaje de placas |  |  |  |  |  |  | **X** | **X** | **X** |  |  |  |  |
| Programación |  |  |  |  |  |  |  |  | **X** | **X** | **X** |  |  |
| Ensamble final |  |  |  |  |  |  |  |  |  |  | **X** | **X** |  |
| Presentación final |  |  |  |  |  |  |  |  |  |  |  |  | **X** |